

EREC G5 Stage 2 Sub-group

Meeting No. 1

To be held at William Gilbert Meeting Room, REEC Building, Sir William Siemens House, Princess Road, Manchester, M20 2UR

On Monday 13th June 2016 10:30-14:30

Agenda

1.	Welcome & Fire Procedure	AS	10:30
2.	Sub-group Chair & Secretary	All	
3.	Terms of Reference (ToR)	Chair	
4.	Review Stage 2 & possible changes <ul style="list-style-type: none"> • Existing Stage 2 approach - overview • Previous suggested text (Aug 2011 draft) • Requirements arising from draft EREC G5/5 Draft V6 <ul style="list-style-type: none"> ○ Alignment with Stage 1 approach ○ Including a 'Compliant with Resonant plant requirement?' ○ Aggregation as per general text. ○ Extension to 100th harmonic. ○ No allocation (except as inferred in the two tables equivalent to Stage 1 tables 13 and 14). ○ Ignore transfer from upstream. ○ No alignment with Stage 2 of IEC TR 61000-3-6. • New considerations (NB Agenda items below) <ul style="list-style-type: none"> ○ Voltage source versus current source ○ Worst case network impedance/ETR112/values of 'k' ○ Aggregation/summation – see notes of full WG ○ $S_i/S_c \leq 0.2\%$ (IEC TR 61000-3-6) 	All	
5.	Agree outline spec for Stage 2 update	All	
6.	Detailed review		
6.1	Worst case network impedance/ETR112/values of 'k' (G5/4-1 Table 8)		
6.2	Simple reactance model & voltage source versus current source		
6.3	Aggregation/summation – see notes of full WG		
6.4	Default fault levels (G5/4-1 Table 9)		
6.5	Current emission limits (G5/4-1 Table 12 & ETR 122)	SPS	
6.6	kVA limits (G5/4/1 Table 10)		
6.7	$S_i/S_c \leq 0.2\%$ (IEC TR 61000-3-6)		
7.	Update spec for Stage 2 update		
8.	Allocate tasks	Chair	
9.	AOB	All	
10.	Future meetings <ul style="list-style-type: none"> • Dates • Agenda items 	Chair	14:30