# EREC G5 Stage 1 & 2 Sub-group

### Meeting No. 6

# To be held at ABB Ltd, Daresbury Park, Warrington WA4 4BT

# On Wednesday 30th November 2016 10:00-15:00

### <u>Agenda</u>

1.	Welcome & Fire Procedure	SPS/FG	10:00
2.	Agree Notes of Previous Meeting	All	
3.	Actions from Meeting 5		
3.1	Definition of Converter Types	AS	
3.2	Harmonic Impedance of LV Networks	AO/AII	
4	Stage 1 & 2 Draft		
4.1	Impact of Siemens Emission Data & (BS) IEC 10002-6	FG	
4.2	Stage 1 & 2 Draft – Update	SPS	
4.3	Draft Fig 2 – New Analysis Spreadsheet	SPS	
4.4	Draft Fig 3 – New Analysis Spreadsheet	SPS	
4.5	Stage 2C – Impact of 77A/926/CDV Compatibility Levels beyond 40th	FGh	
4.6	Harmonic Impedance of LV Networks	All	
4.7	Stage 1 & 2 Draft Worked Examples - Update	SPS	
4.8	Stage 2C Thevenin Equivalent Example	FG	
5	Agree Further Work	All	
6	AOB	All	
7	Future meetings	All	
	• Dates		
	Agenda items		
8	Close	SPS	15:00